

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	107	(mosfet cmos mos) with source with drain with ("same" equival\$4 chang\$4) with (known skill\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/17 12:53
L5	10	(mosfet cmos mos) adj3 source near2 drain with ("same" equival\$4) with (known skill\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/17 14:30
S1	698	327/544.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/04/10 16:44
S4	696	(header footer) with drains with (coupled connected)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/04/10 16:55
S5	13	(header footer) with drains with (coupled connected) same substrate	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/04/10 16:55
S6	13	(header) with nmos with pmos	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/04/10 16:59
S7	1	(header) with nmos with pmos with seri\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/04/10 16:59
S22	8	stack\$4 adj (power source supply) adj transistors	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/04/14 13:16
S23	113	stack\$4 adj3 (power source supply) adj3 transistors	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/04/14 13:17
S26	4237	(nmos nmosfet nfet n\$1mos n\$1mosfet n\$1fet) with (pmos pmosfet pfet p\$1mos p\$1mosfet p\$1fet) with (stacked cascode seri\$4) with (power supply source voltage vint internal vperi vcore peripheral core)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 09:03

S27	4472	(nmos nmosfet nfet n \$1mos n\$1mosfet n\$1fet) with (pmos pmosfet pfet p \$1mos p\$1mosfet p\$1fet) with (stacked cascode seri \$4) with (ground power supply source voltage vint internal vperi vcore peripheral core)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 09:04
S28	123	S27 same (charge voltage current) near2 (pump\$4 multiplication multipl\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 09:07
S29	4349	S27 not S28	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 09:19
S30	852	S29 and (charge voltage current) near2 (pump\$4 multiplication multipl\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 09:19
S32	377	327/437.cds.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 10:14
S39	102	327/436.cds.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 10:48
S44	3842	(nmos nmosfet nfet n \$1mos n\$1mosfet n\$1fet n\$2type) with (pmos pmosfet pfet p\$1mos p \$1mosfet p\$1fet p \$2type) with (stacked cascode seri\$4) with ((power supply) near2 (source voltage) ground vint internal vperi vcore peripheral core vdd voc vss gnd)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 11:29
S45	957	S44 and (pump\$4 boost \$4)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 11:30
S46	379	S45 not (S28 S30)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 11:31
S48	22	mtcmos and pump	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/04/15 11:51
S50	78	mtcmos with ((power supply) near2 (source voltage))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2008/04/15 11:56

S55	7043	virtual adj (vss ground) virtual\$1ground virtual \$1vss	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 12:39
S56	6702	(nmos nmosfet nfet n \$1mos n\$1mosfet n\$1fet) with (pmos pmosfet pfet p \$1mos p\$1mosfet p\$1fet) with (stacked cascode seri \$4)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 12:39
S66	72	(nmos nmosfet nfet n \$1mos n\$1mosfet n\$1fet) with (pmos pmosfet pfet p \$1mos p\$1mosfet p\$1fet) with (sleep)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 14:20
S79	16	(nfet nmos nmos nmosfet n\$1fet n\$1mos n \$1mosfet) adj2 source adj2 (vcc vdd (power supply) near (voltage source)) with (pmosfet pmos pmos p\$1mosfet p \$1mos p\$1fet)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 15:05
S81	35	(nfet nmos nmos nmosfet n\$1fet n\$1mos n \$1mosfet) adj2 source adj2 (vcc vdd (power supply) near (voltage source))	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 15:20
S82	52	(nfet nmos nmos nmosfet n\$1fet n\$1mos n \$1mosfet) adj2 drain adj3 source adj2 (pmos pfet pmos p\$1fet P\$1mos P\$1mosfet)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 15:27
S84	124	(pmos pfet pmos p\$1fet P \$1mos P\$1mosfet) with (vground virtual near2 (vss ground gnd) vvss)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 15:35
S87	141	327/530-544.ccls. and (virtual near2 (ground vss vdd vcc (power voltage supply) near (voltage source power) vvcc vvdd vvss vgnd) header footer)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 15:49
S97	1053	326/31,33,34.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 16:53
S100	73549	nmos source adj2 (connect\$4 coupl\$4) adj2 source	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 17:18

S103	104	nmos adj2 gate adj2 (connect\$4 coupl\$4) adj2 source	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 17:19
S104	37	nmos adj2 gate adj2 (connect\$4 coupl\$4) adj2 source with pmos	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/15 17:19
S117	2214	327/534,535,357.ccls. not (326/31,33,34.ccls. 327/544.ccls.)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/16 11:24
S119	1522	(pump\$4 boost\$4) with gate with (nmos nmosfet nfet n\$1mos n\$1mosfet n \$1fet n\$2type)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/16 11:42
S120	59582	(pmos pfet pmos p\$1fet P \$1mos P\$1mosfet)	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/16 11:42
S121	1543	(pmos pfet pmos p\$1fet P \$1mos P\$1mosfet) with source adj3 (connect\$4 coupl\$4) adj3 gate	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/16 11:43
S122	20	S119 same S121	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/16 11:44
S125	402	mtcmos mt adj cmos mt \$1coms	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/16 13:05
S126	402	mtcmos mt adj cmos mt \$1cmos	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/16 13:05
S138	562	327/434.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2008/04/16 15:12

4/ 17/ 2008 3:09:38 PM

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